ASYNC 2015

Welcome

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Ian W. Jones
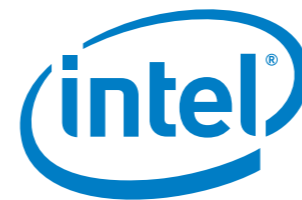
Async 2015

thanks to:

- Authors
- Reviewers
- Organizers
- Sponsors
- Attendees

Async 2015 sponsors:

Program chairs:

Jens Sparsø	DTU, Denmark
Eslam Yahya	Benha University, Egypt

Industrial Workshop chairs:

Andrew Lines	Intel Labs, USA
Arash Saifhashemi	Intel Labs, USA
Mehrdad Najibi	Cadence, USA

Best Paper Award chair:

Andreas Steininger   Vienna University of Technology, Austria

Types of Papers

- Regular papers (8 pages)
  - In proceedings and IEEE Explore
  - Reviewed by program committee

- Industry papers (2 pages)
  - In proceedings and IEEE Explore
  - Reviewed by program chairs and industrial workshop chairs.

- Fresh ideas (2 pages) + Demos and Posters
  - In separate booklet

Submission Statistics

- Regular papers:
  - 65 Papers registered (title + abstract)
  - 50 Papers submitted (complete papers)
  - 18 Papers accepted

- Industry papers:
  - 2 papers submitted and accepted

- Fresh ideas papers:
  - 9 papers submitted, 6 accepted

The Review Process

- 4 reviews per paper

- 40 reviewers / PC members
  + 47 secondary reviewers

- 5-(6) papers per reviewer

- 18 of the 50 regular papers accepted   (36 %)

- The 32 rejected papers: comments and suggestions
  for improvements

Submission Statistics

Regular papers:

Australia	1	Iran	1
Austria	2	Israel	1
Brazil	1	Italy	1
Canada	1	Japan	2
China	1	Taiwan	1
Egypt	1	The Netherlands	1
France	2	UK	5
Germany	3	USA	24
India	2		

The Program

- 3 keynotes

- 18 regular papers

- 2 industry papers

- 6 fresh ideas

- Demos and posters

Monday – Industrial Workshop

Keynote 1:

- Bob Iannucci, CMU Silicon Valley
  Toward Platformization for the Internet of Things

Paper sessions

- Session 1: Clock Domain Crossing

- Session 2: New Circuits, Real Silicon

- Session 3: Demos
  - CAD Tools, Chips, and Posters

Tuesday

Paper sessions

- Session 4: Physical Design and Optimization

- Session 5: New Perspectives

- Session 6: Cryptoprocessors and NOC

- Session 7: Fresh Ideas

Keynote 2 (at conference dinner):

- Paul Cunningham and Steev Wilcox, Cadence
  A Random Walk from Async to Sync

Wednesday

Keynote 3:

- Ron Ho, Altera
  Timing in the Wild: Synchronization, Scale, and FPGAs

Paper sessions

- Session 8: Merge, Mutual Exclusion, and Arbitration

- Session 9: Emerging Technologies

  – Break: Vote for best paper –

- Closing session + Best paper award

Best Paper Award: Committee

- Rostislav (Reuven) Dobkin
  - Technion, Israel

- Joycee Meckie
  - Indian Institute of Technology Gandhinagar, India

- Hiroshi Saito,
  - The University of Aizu, Japan

- Montek Singh
  - University of North Carolina, US

- Andreas Steininger (chair)
  - Vienna University of Technology, Austria

Best Paper Award: Procedure

- All accepted regular papers were candidates
  - submission, reviews & discussion, final version

- Nomination phase

- Discussion phase
  - contribution, expected impact, degree of innovation,…

- Voting for finalists

- Voting for winner ⇐ here at the conference
  - decision based on your votes!

Best Paper Award: Finalists

- Session 5:

  Analyzing Isochronic Forks with Potential Causality

  R. Manohar, Y. Moses

- Session 8 :

  Increasing Impartiality and Robustness in High-Performance N-Way Asynchronous Arbiters

  G. Miorandi, D. Bertozzi, S. M. Nowick

- Session 9:

  Non-Volatility For Ultra-Low Power Asynchronous Circuits in Hybrid CMOS/Magnetic Technology

  E. Zianbetov, E. Beigné, G. Di Pendina

Best Paper Award: Winner

- Announcement of winner in the closing session Wednesday 3 pm

- All finalists receive a certificate

- Winner receives
  - a special plaque (design: Ian W. Jones)
  - a GPU sponsored by Nvidia