

PROFESSIONAL ACTIVITIES

Membership in Professional Societies

1. Fellow, The Institute of Electrical & Electronics Engineers (IEEE).
2. Fellow, American Association for the Advancement of Science (AAAS).
3. Fellow, The International Society for Optical Engineering (SPIE).
4. Member, European Association for Signal Processing (EURASIP).

Editorial Activities

1. Editor, IEEE Circuit Theory Group Newsletter - From February 1968 to August 1969.
2. Associate Editor, IEEE Transactions on Circuit Theory - From July 1969 to June 1971.
3. Associate Guest Editor, IEEE Transactions on Circuit Theory, Special Issue on "Active and Digital Networks," November 1971.
4. Member of the Editorial Board, IEEE Press, 1972-1976.
5. Guest Editor, IEEE Transactions on Education, Special Issue on "Continuing Education," May 1973.
6. Member of the Editorial Board, IEEE Proceedings, 1977- 1982.
7. Guest Editor, IEEE Proceedings, Special Issue on "Miniaturized Filters," January 1979.
8. Consulting Editor, VNR Series on Electrical Engineering and Computer Science, Van Nostrand Reinhold Company, New York - From September 1977 to December 1987.
9. Editorial Consultant to CRC Press, John Wiley & Sons, McGraw-Hill Book Co., Prentice-Hall, and Addison-Wesley.
10. Editorial Reviewer for IEEE Proceedings; IEEE Transactions on Circuits and Systems; IEEE Transactions on Signal Processing; IEEE Transactions on Education; International Journal on Circuit Theory and Applications; Journal of the Franklin Institute; Computer Vision, Graphics, and Image Processing; Applied Optics; Electronics Letters (U.K.); IEE Proceedings (U.K.).
11. Reviewer of research proposals for the National Science Foundation; U.S. Army Research Office; California Space Institute of the University of California; Natural Sciences and Engineering Research Council of Canada; Australian Research Council; University of California MICRO Committee; Fonds National Suisse de la Recherche Scientifique (Switzerland); Hong Kong Research Grants Council; Fonds pour la Formation de Chercheurs et l'Aide á la Recherche (Quebec, Canada).
12. Associate Editor, International Journal on "Circuits, Systems, and Signal Processing" - From January 1982 to December 1999.
13. Guest Editor, IEEE Proceedings, Special Issue on "Hardwares and Softwares for Digital Signal Processing", September 1987.
14. Member of the Editorial Board of "Signal Processing" - the Journal of the European Association for Signal and Image Processing - From July 1988 to May 2005.

15. Associate Editor, International Journal on "Multidimensional Systems and Signal Processing" - From January 1990 to December 2002.
16. Co-Guest Editor, International Journal on Multidimensional Systems and Signal Processing, Special Issue on "Multidimensional Processing of Video Signals", December 1991.
17. Overseas Advisor, IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, The Institute of Electronics, Information and Communication Engineers (Japan), January 1991 to December 2001.
18. Member of the Editorial Board, The Journal of the Franklin Institute, January 1994 to present.
19. Member of the Editorial Board, Automatika, Croatian Society for Communications, Computing, Electronics, Measurement and Control, Zagreb, Croatia, 1996 - present.
16. Co-Guest Editor, Special Issue on "Objective Video Quality Metrics," Image Communication, January 2004.
17. Member, Editorial Council, Ingeniería, UNAM, Mexico City, Mexico, January 2004 to present.
18. Member, Editorial Board, IEEE Signal Processing Magazine (2005-2007).

External Examiner for Doctoral Dissertations

1. Department of Electrical Engineering, University of Calgary, Calgary, Alberta, Canada, January 1972.
2. Department of Electrical Engineering, Concordia University, Montreal, Canada, September 1972.
3. Department of Electrical Engineering, Indian Institute of Science, Bangalore, India, November 1972.
4. Department of Electrical Engineering, Indian Institute of Technology, Kanpur, India, January 1973.
5. Department of Electrical Engineering, University of Toronto, Toronto, Canada, January 1977.
6. Department of Electronics and Communication Engineering, Jadavpur University, Jadavpur, India, September 1978.
7. Department of Electrical Engineering and Computer Science, University of New South Wales, Kensington, NSW, Australia, January 1983.
8. Department of Electrical Engineering, Tampere University of Technology, Tampere, Finland, June 1984.
9. Department of Electrical and Computer Engineering, Concordia University, Montreal, Canada, June 1985.
10. Department of Electrical Engineering, Monash University, Clayton, Victoria, Australia, December 1985.
11. Department of Systems Engineering, The Australian National University, Canberra, ACT, Australia, December 1991.
12. Division of Engineering Science, Simon Fraser University, Burnaby B.C., Canada, October 1996.

13. Department of Electrical Engineering, Norwegian University of Science & Technology, Trondheim, Norway, August 1998.
14. Department of Electrical Engineering, Indian Institute of Science, Bangalore, India, November 1998.
15. Department of Electrical Engineering, Swiss Federal University of Technology, Lausanne, Switzerland, October 2000.
16. Department of Electrical Engineering, Swiss Federal University of Technology, Lausanne, Switzerland, November 2005.

Short Course Activities

1. Course Organizer and Lecturer, "Active Filter Design: Theory and Practice," University of California, Los Angeles, California, September 1969.
2. Guest Lecturer, "Modern Filter Design Techniques," University of California, Los Angeles, California, April 1970.
3. Guest Lecturer, "Active Filters," Virginia Polytechnic Institute, Blacksburg, Virginia, July 1970.
4. Course Organizer and Lecturer, "Active Inductorless Filters," IEEE Circuit Theory Group Chapter, San Francisco, California, January 1971.
5. Course Organizer and Lecturer, "Active Inductorless Filters," IEEE Circuit Theory Group Chapter, San Francisco, California, November 1971.
6. Guest Lecturer, "Active Filters," George Washington University, Washington, D.C., December 1971.
7. Course Organizer and Lecturer, "Active Inductorless Filters," IEEE Educational Activities Board, New York, March 1972.
8. Guest Lecturer, "Active Filters," George Washington University, Washington, D.C., December 1972.
9. Course Organizer and Lecturer, "Digital Filters," George Washington University, Washington, D.C., December 1972.
10. Guest Lecturer, "Active Networks and Computer-Aided Design," Indian Institute of Science, Bangalore, India, January 1973.
11. Course Co-Organizer and Lecturer, "Active and Digital Filters," Bharat Electronics, Bangalore, India, March 1973.
12. Course Organizer and Lecturer, "Digital Filters," George Washington University, Washington, D.C., March 1974.
13. Course Organizer and Lecturer, "Digital Processing of Signals," University of California, Davis, California, June 1974.
14. Course Co-Organizer and Lecturer, "Modern Filter Theory and Design Techniques," University of California, Los Angeles, California, September 1974.

15. Guest Lecturer, "Digital Filters," Scientific Engineering Systems, Inc. and the University of Nevada, Reno, Nevada, September 1976.
16. Guest Lecturer, "Active and Digital Filter Design," University of California, Berkeley, California, January 1977.
17. Guest Lecturer, "Two-Dimensional Digital Signal Processing," Rensselaer Polytechnic Institute, Troy, New York, July 1977.
18. Co-Organizer and Lecturer, "Digital Filter Theory and Design," University of California, Berkeley, California, November 1977.
19. Coordinator and Lecturer, "Multidimensional Signal Processing," University of California, Los Angeles, California, February 1979.
20. Lecturer, "Integrated Filters," University of California, Los Angeles, California, October 1979.
21. Lecturer, "Advanced Digital Signal Processing," INSKO, Helsinki, Finland, May 1987.
22. Lecturer, Tutorial on "Design of Computationally Efficient FIR and IIR Digital Filters," ISSPA 87 - International Symposium on Signal Processing and Its Applications, Brisbane, Queensland, Australia, August 1987.
23. Guest Lecturer, "Visual Information Transmission and Technology," U.S. West Advanced Technologies, Englewood, CO, June 1989.
24. Lecturer, Tutorial on "Digital Signal Processing," Ninth IEEE Annual International Phoenix Conference on Computers and Communications, Scottsdale, AZ, March 1990.
25. Lecturer, Tutorial on "Multirate Digital Signal Processing," ISSPA 90 - International Symposium on Signal Processing and Its Applications, Gold Coast, Queensland, Australia, August 1990.
26. Lecturer, Tutorial on "Multirate Digital Signal Processing," 38th Midwest Symposium on Circuits & Systems, Rio de Janeiro, Brazil, August 1995.
27. Lecturer, Tutorial on "Digital Filter Design and Implementation," Jawaharlal Nehru Centre for Advanced Scientific Research, Indian Institute of Science, Bangalore, India, November 1998.
28. Lecturer, Tutorial on "Digital Filter Design and Implementation," IEEE Workshop on Nonlinear Signal & Image Processing, Antalya, Turkey, June 1999.
29. Lecturer, Tutorial on "Digital Filter Design and Implementation," American University of Beirut, Beirut, Lebanon, May 2000.
30. Lecturer, Tutorial on "Digital Filter Design and Implementation," Isik University, Istanbul, Turkey, June 2000.
31. Lecturer, Tutorial on "Digital Filter Design and Implementation," 8th IEEE International Conference on Electronics, Circuits & Systems, Malta, September 2001.
32. Lecturer, Tutorial on "Multirate Digital Signal Processing," Departamento de Ingeniería de Sistemas Computacionales y Automatización (DISCA), IIMAS, National Autonomous University of Mexico, Mexico City, Mexico, November 2001.
33. Lecturer, Tutorial on "Digital Filter Design Using MATLAB," 17th European Conference on Circuit Theory and Design, Cork, Ireland, August-September 2005.
34. Lecturer, Tutorial on "Digital Filter Design Using MATLAB," Institute of Radio Physics and Electronics, Calcutta University, Kolkata, India, December 2005.

Conference Activities

1. Member of Technical Program Committee, 1968 IEEE International Symposium on Circuit Theory, Miami Beach, Florida.
2. Member of Organizing Committee and Chairman of Local Arrangements Committee, 1969 IEEE International Symposium on Circuit Theory, San Francisco, California.
3. Session Chairman, Third Asilomar Conference on Circuits and Systems, Pacific Grove, California, December 1969.
4. Program Chairman, Fourth Asilomar Conference on Circuits and Systems, Pacific Grove, California, November 1970.
5. Session Chairman, Kyoto International Conference on Circuit Theory, Kyoto, Japan, September 1970.
6. General Chairman, Fifth Asilomar Conference on Circuits and Systems, Pacific Grove, California, November 1971.
7. Session Organizer and Chairman of Session "Inductorless Filters," IEEE WESCON Convention, San Francisco, California, August 1971.
8. Member of Technical Program Committee, Mexico 1971 IEEE International Conference on Circuits, Systems, and Computers, Oaxtepec, Mexico, January 1971.
9. Session Organizer and Chairman of Session "Active Filters," Mexico 1971 IEEE International Conference on Circuits, Systems, and Computers, Oaxtepec, Mexico, January 1971.
10. Session Organizer and Chairman of Session "Miniaturized Filters," IEEE International Convention, New York, New York, March 1972.
11. Chairman of Tutorial and Technical Applications Committee, and Member of Technical Program Committee, 1972 IEEE International Symposium on Circuit Theory, Los Angeles, California, April 1972.
12. Session Chairman, 1972 IEEE International Symposium on Circuit Theory, Los Angeles, California, April 1972.
13. Member of International Scientific Committee, The Second International Symposium on Network Theory, Yugoslavia, July 1972.
14. Session Chairman, Conference on Circuits and Systems, Osmania University, Hyderabad, India, February 1973.
15. Session Chairman, 1973 IEEE International Symposium on Circuit Theory, Toronto, Canada, April 1973.
16. General Chairman, 1974 IEEE International Symposium on Circuits and Systems, San Francisco, California, April 1974.
17. Correspondence Member of the Organizing Committee, 1974 European Circuit Theory and Design Conference, London, England, July 1974.

18. Session Organizer, Special Invited Session on "Applications of Digital Signal Processing," 8th Asilomar Conference on Circuits, Systems, and Computers, Pacific Grove, California, December 1974.
19. Member of the International Scientific Committee, Third International Symposium on Network Theory, Yugoslavia, September 1975.
20. Session Organizer and Chairman, Special Invited Session on "Circuits and Computers," Second Interamerican Conference on Systems and Informatics, Mexico City, Mexico, November 1974.
21. Member of Technical Program Committee, 1975 IEEE International Symposium on Circuits and Systems, Boston, Massachusetts, April 1975.
22. Session Organizer of Special Invited Session on "Two-Dimensional Digital Signal Processing," 1975 IEEE International Symposium on Circuits and Systems, Boston, Massachusetts, April 1975.
23. Member of Technical Program Committee, and Member of International Publicity Committee, 1976 IEEE International Symposium on Circuits and Systems, Munich, West Germany, April 1976.
24. Session Chairman, 1976 IEEE International Symposium on Circuits and Systems, Munich, West Germany, April 1976.
25. Member of International Publicity Committee, 1977 IEEE International Symposium on Circuits and Systems, Phoenix, Arizona, April 1977.
26. Session Chairman, 1977 IEEE International Symposium on Circuits and Systems, Phoenix, Arizona, April 1977.
27. Session Organizer and Chairman, Special Invited Session on "Miniaturized Filters," 1978 IEEE International Symposium on Circuits and Systems, New York City, New York, May 1978.
28. Session Organizer and Chairman, Special Invited Session on "Two-Dimensional Digital Filters," 1978 European Conference on Circuit Theory and Design, Lausanne, Switzerland, September 1978.
29. Member of Technical Program Committee, Third Florence Conference on Digital Signal Processing, Florence, Italy, August 1978.
30. Session Organizer and Chairman, Session on "Design and Applications of Multi-Dimensional Digital Filters," 1979 IEEE International Symposium on Circuits and Systems, Tokyo, Japan, July 1979.
31. Member, International Publicity Committee, 1979 IEEE International Symposium on Circuits and Systems, Tokyo, Japan, July 1979.
32. Member of Steering Committee and Session Chairman, First IEEE Workshop on Two-Dimensional Digital Signal Processing, Berkeley, California, October 1979.
33. Session Chairman, 1980 IEEE International Conference on Acoustics, Speech, and Signal Processing, Denver, Colorado, April 1980.
34. Member, International Scientific Committee, Fourth International Symposium on Network Theory, Yugoslavia, September 1980.
35. Session Organizer and Chairman, Special Session on "Digital Filter Structures and Algorithms," 14th Asilomar Conference on Circuits, Systems, and Computers, Pacific Grove, California, November 1980.
36. Session Chairman, 1981 IEEE International Symposium on Circuits and Systems, Chicago, Illinois, April 1981.

37. Member of Technical Program Committee, International Conference on Digital Signal Processing, Florence, Italy, September 1981.
38. Session Chairman, Session on "Digital Filters and Design Methods II," International Conference on Digital Signal Processing, Florence, Italy, September 1981.
39. Member of International Scientific Committee, European Conference on Circuit Theory and Design, The Hague, The Netherlands, August 1981.
40. Session Chairman, International Conference on Communication Circuits and Systems, Calcutta, India, December 1981.
41. Session Organizer and Chairman, Special Invited Session on "Digital Signal Processing - II," IFAC Symposium on Theory and Applications of Digital Control, New Delhi, India, January 1982.
42. Member, Technical Program Committee, 1982 IEEE International Symposium on Circuits and Systems, Rome, Italy, April 1982.
43. Member, International Scientific Committee, 1982 Summer Symposium on Circuit Theory, Prague, Czechoslovakia, September 1982.
44. Session Chairman, Session on "Quantization Effects," 1983 IEEE International Conference on Acoustics, Speech, and Signal Processing, Boston, Massachusetts, April 1983.
45. Chairman, Technical Program Committee, 1983 IEEE International Symposium on Circuits and Systems, Newport Beach, California, May 1983.
46. Member, Technical Program Committee, 1983 European Conference on Circuit Theory and Design, Stuttgart, West Germany, September 1983.
47. Session Chairman, Session on "Multidimensional Digital Filters," 1983 European Conference on Circuit Theory and Design, Stuttgart, West Germany, September 1983.
48. Far East Liaison, 1984 IEEE International Conference on Acoustics, Speech, and Signal Processing, San Diego, California, April 1984.
49. Session Chairman, Session on "Multidimensional Systems," 1984 IEEE International Symposium on Circuits and Systems, Montreal, Canada, May 1984.
50. Member, International Scientific Committee, 5th International Symposium on Network Theory, Yugoslavia, September 1984.
51. Member of Technical Program Committee, International Conference on Digital Signal Processing, Florence, Italy, September 1984.
52. Member, Advisory Committee, International Conference on Computers, Systems, and Signal Processing, Bangalore, India, December 1984.
53. Session Organizer and Chairman, Session on "An Overview of Signal Processing and Communications," International Conference on Computers, Systems, and Signal Processing, Bangalore, India, December 1984.
54. Member, Steering Committee and Session Chairman, IEEE International Workshop on Digital Signal processing, Kyoto, Japan, June 1985.
55. Member, Publicity Committee, 1985 International Symposium on Circuits and Systems, Kyoto, Japan, June 1985.

56. Session Chairman, Session on "Digital Filters," 1985 International Symposium on Circuits and Systems, Kyoto, Japan, June 1985.
57. Member, Technical Program Committee, 1985 China International Symposium on Circuits and Systems, Beijing, China, June 1985.
58. Session Chairman, Session on "Digital Filters and Processing," 1985 China International Symposium on Circuits and Systems, Beijing, China, June 1985.
59. Member, International Scientific Committee, 1985 European Conference on Circuit Theory and Design, Prague, Czechoslovakia, September 1985.
60. Technical Program Chairman, Fifth Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, April 1986.
61. Member, Technical Program Committee, 1986 IEEE International Symposium on Circuits and Systems, Santa Clara, California, May 1986.
62. Session Chairman, Session on "Digital Filters," 1986 IEEE International Symposium on Circuits and Systems, Santa Clara, California, May 1986.
63. Session Chairman, Session on "Digital Signal Processing," ISSPA 87 - International Symposium on Signal Processing and Its Applications, Brisbane, Queensland, Australia, August 1987.
64. Member, International Scientific Committee, 1987 European Conference on Circuit Theory and Design, Paris, France, September 1987.
65. Member, Technical Program Committee, International Conference on Digital Signal Processing, Florence, Italy, September 1987.
66. Technical Program Chairman, Sixth Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, November 1987.
67. Session Chairman, Sixth Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, November 1987.
68. Member, International Program Committee, 1987 International Symposium on Electronic Devices, Circuits & Systems - ISELDECS'87, Kharagpur, India, December 1987.
69. Session Chairman, Session on "Pattern Recognition," 1987 International Symposium on Electronic Devices, Circuits & Systems - ISELDECS'87, Kharagpur, India, December 1987.
70. Session Chairman, Session on "Multidimensional Systems," 1988 IEEE International Conference on Acoustics, Speech & Signal Processing, New York, NY, April 1988.
71. Member, Technical Program Committee, 1988 IEEE International Symposium on Circuits and Systems, Helsinki, Finland, June 1988.
72. Session Chairman, Session on "Image Enhancement," 1988 IEEE International Symposium on Circuits and Systems, Helsinki, Finland, June 1988.
73. Member, Technical Program Committee, International Conference on Advances in Communication and Control Systems, Baton Rouge, Louisiana, October 1988.
74. Session Organizer, Session on "New Directions in Communications and Signal Processing," International Conference on Advances in Communication and Control Systems, Baton Rouge, Louisiana, October 1988.

75. General Chairman, Seventh Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, November 1988.
76. Moderator, Panel Discussion on "Future Directions of Digital Signal Processing", Seventh Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, November 1988.
77. Panelist, Panel Discussion on "Future Directions of Circuits & Systems", 1989 IEEE International Symposium on Circuits & Systems, Portland, Oregon, May 1989.
78. Session Chairman, Session on "Motion and Image Sequence Coding", 1989 IEEE International Conference on Acoustics, Speech and Signal Processing, Glasgow, Scotland, U.K., May 1989.
79. Member, International Scientific Committee, The 6th International Symposium on Networks, Systems and Signal Processing, Zagreb, Croatia, June 1989.
80. Session Chairman, Session on "Digital Filters and Multirate DSP," The 6th International Symposium on Networks, Systems and Signal Processing, Zagreb, Croatia, June 1989.
81. Chairman, Publicity Committee, The 8th Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, July 1989.
82. Session Organizer and Chairman, Session on "Hardwares and Softwares for Digital Image Processing," IEEE International Conference on Systems Engineering, Dayton, Ohio, August 1989.
83. Member, International Scientific Committee, 1989 European Conference on Circuit Theory and Design, Brighton, United Kingdom, September 1989.
84. Session Chairman, Session on "Digital Signal Processing I," 1989 European Conference on Circuit Theory and Design, Brighton, United Kingdom, September 1989.
85. Keynote Speaker, URSI/IEEE 15th Convention on Radio Science, Tampere, Finland, November 1989.
86. Session Chairman, Session on "Image Processing/Analysis Algorithms," 1990 SPIE/SPSE Symposium on Electronic Imaging, Santa Clara, CA, February 1990.
87. Member, Technical Program Committee, 9th IEEE Annual International Phoenix Conference on Computers and Communications, Phoenix, Arizona, March 1990.
88. Session Chairman, Session on "Imaging Architectures," 9th International Phoenix Conference on Computers and Communications, Phoenix, Arizona, March 1990.
89. Chairman, International Coordination Committee, 1990 IEEE International Symposium on Circuits & Systems, New Orleans, LA, May 1990.
90. Session Chairman, Session on "Digital Signal Processing," 1990 Bilkent International Conference on New Trends in Communication, Control, and Signal Processing - BILCON'90, Ankara, Turkey, July 1990.
91. Member, Technical Program Committee, ISSPA 90 - International Symposium on Signal Processing and Its Applications, Brisbane, Queensland, Australia, August 1990.
92. Keynote Speaker, ISSPA 90 - International Symposium on Signal Processing and Its Applications, Gold Coast, Queensland, Australia, August 1990.
93. Session Chairman, ISSPA 90 - International Symposium on Signal Processing and Its Applications, Gold Coast, Queensland, Australia, August 1990.

94. Co-Chairman, SPIE/SPSE Conference on Image Processing Algorithms and Techniques II, San Jose, CA, February 1991.
95. Member, Technical Program Committee, 1991 IEEE International Symposium on Circuits & Systems, Singapore, June 1991.
96. Session Chairman, Session on "Median Filters," 1991 IEEE International Symposium on Circuits & Systems, Singapore, June 1991.
97. General Chairman, The 9th Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, June 1991.
98. Member, Technical Program Committee, The 9th Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, June 1991.
99. Session Chairman, The 9th Kobe International Symposium on Electronic and Information Sciences, Kobe, Japan, June 1991.
100. Member, Technical Program Committee, 1991 International Conference on Digital Signal Processing, Florence, Italy, September 1991.
101. Member, International Scientific Committee, 1991 European Conference on Circuit Theory and Design, Copenhagen, Denmark, September 1991.
102. Member, Program Committee, IEEE CompEuro'92, The Hague, The Netherlands, May 1992.
103. Chairman, International Coordination Committee, 1992 IEEE International Symposium on Circuits & Systems, San Diego, CA, May 1992.
104. Member, Technical Program Committee, 1992 IEEE International Symposium on Circuits & Systems, San Diego, CA, May 1992.
105. Co-Organizer, Special Session on "Multirate Signal Processing: Analog and Digital," 1992 IEEE International Symposium on Circuits & Systems, San Diego, CA, May 1992.
106. Chairman, UCSB/CIPR Conference on Advances in Communications and Signal Processing, May 1992.
107. Member, Technical Program Committee, ASPA'92 - International Workshop on Application Specific Computing, Berkeley, CA, August 1992.
108. Member, International Scientific Committee, Sixth European Signal Processing Conference - EUSIPCO'92, Brussels, Belgium, August 1992.
109. Co-Organizer and Session Chairman, Special Session on "Multirate Digital Filter Banks: Theory and Applications", Sixth European Signal Processing Conference - EUSIPCO'92, Brussels, Belgium, August 1992.
110. Member, Technical Program Committee and Overseas Liaison, ISSPA 92 - International Symposium on Signal Processing and Its Applications, Brisbane, Queensland, Australia, August 1992.
111. Member, International Program Committee, 1992 IEEE International Conference on Systems Engineering, Kobe, Japan, September 1992.
112. Vice-Chairman, Technical Program Committee, 1992 IEEE International Conference on Systems Engineering, Kobe, Japan, September 1992.

113. Member, Program Committee, 38th Annual International Conference KoREMA, Zagreb, Croatia, April 1993.
114. Member, Technical Program Committee, 1993 IEEE International Symposium on Circuits & Systems, Chicago, IL, May 1993.
115. Member, Technical Program Committee, International Conference on Image Processing: Theory & Applications, San Remo, Italy, June 1993.
116. Session Chairman, International Conference on Image Processing: Theory & Applications, San Remo, Italy, June 1993.
117. Session Chairman, 4th International Workshop on Time-Varying Image Processing and Moving Object Recognition, Florence, Italy, June 1993.
118. Member, International Program Committee, International Conference on Digital Signal Processing, Nicosia, Cyprus, July 1993.
119. Member, International Advisory Committee, 3rd IEEE Workshop on Visual Signal Processing and Communications, Melbourne, Australia, September 1993.
120. Session Organizer and Chairman, Special Session on "Nonlinear Signal Processing," 27th Annual Asilomar Conference on Signals, Systems, and Computers, Pacific Grove, CA, November 1993.
121. Chairman, UCSB/CIPR Workshop on Nonlinear Signal Processing, November 1993.
122. Member, Technical Program Committee, International Workshop on Digital Video for Intelligent Systems, Irvine, CA, December 1993.
123. Session Chairman, Session on "Imaging Systems," 1994 International Conference on Image and Speech Processing, and Neural Networks, Hong Kong, April 1994.
124. Session Chairman, Session on "Digital Signal Processing II, 1994 IEEE International Symposium on Circuits & Systems, London, U.K., June 1994.
125. Session Chairman, Session on "Parallel and Distributed Processing," NASA Applied Information Systems Research Program (AISRP) Workshop, Boulder, CO, July 1994.
126. Member, Technical Program Committee, 37th Midwest Symposium on Circuits & Systems, Lafayette, LA, August 1994.
127. Member, Organizing Committee, Technology-Based Partnership Conference and 7th Annual Engineering Research Review, UCSB, February 1995.
128. Organizer and Session Chairman, Forum Session on "Should DSP Replace Circuit Theory as the First Course", IEEE International Symposium on Circuits & Systems, Seattle, WA, May 1995.
129. Member, Technical Program Committee, 1995 IEEE Workshop on Nonlinear Digital Signal and Image Processing, Neos Marmaras-Halkidiki, Greece, June 1995.
130. Session Chairman, Session on "Volterra Filters," 1995 IEEE Workshop on Nonlinear Digital Signal and Image Processing, Neos Marmaras-Halkidiki, Greece, June 1995.
131. Member, International Program Committee, International Conference on Digital Signal Processing, Limassol, Cyprus, June 1995.
132. Session Organizer & Chairman, Session on "Nonlinear Image Processing," International Conference on Digital Signal Processing, Limassol, Cyprus, June 1995.

133. Panelist, Panel Discussion on "The Economic Development of Technology," Science & Engineering Council of Santa Barbara, August 1995.
134. Member, Technical Program Committee, 38th Midwest Symposium on Circuits & Systems, Rio de Janeiro, Brazil, August 1995.
135. Session Chairman, Session on "Adaptive Filters," 38th Midwest Symposium on Circuits & Systems, Rio de Janeiro, Brazil, August 1995.
136. Keynote Speaker, 38th Midwest Symposium on Circuits & Systems, Rio de Janeiro, Brazil, August 1995.
137. Member, Steering Committee, 5th International Workshop on Time-Varying Image Processing and Moving Object Recognition, Florence, Italy, September 1995.
138. Member, Conference Committee, SPIE Conference on Digital Image Storage and Archiving Systems, Philadelphia, PA, October 1995.
139. Member, Technical Committee, Photonics East'95 - SPIE International Symposium on Information, Communications and Computer Technology, Applications and Systems, Philadelphia, PA, October 1995.
140. Member, Technical Program Committee, 1995 IEEE International Conference on Image Processing, Washington, DC, October 1995.
141. Member, Technical Program Committee, ISSPA 96 - International Symposium on Signal Processing and Its Applications, Gold Coast, Queensland, Australia, August 1996.
142. Member, Technical Program Committee, 39th Midwest Symposium on Circuits & Systems, Ames, Iowa, August 1996.
143. Member, International Program Committee, KoREMA'96 - 41st Annual Conference, Opatija, Croatia, September 1996.
144. Member of Steering Committee and Session Chairman, 5th International Workshop on Time-Varying Image Processing and Moving Object Recognition, Florence, Italy, September 1996.
145. Member, Technical Program Committee, 1996 European Signal Processing Conference (EUSIPCO'96), Trieste, Italy, September 1996.
146. Keynote Speaker, 1996 European Signal Processing Conference (EUSIPCO'96), Trieste, Italy, September 1996.
147. Session Organizer and Chairman, Session on "Recent Trends in Multirate Signal Processing", and Session Chairman, Session on "Image Restoration I", 1996 European Signal Processing Conference (EUSIPCO'96), Trieste, Italy, September 1996.
148. Member, Technical Program Committee, and Session Chairman of Session "Post-coder filtering," 1996 IEEE International Conference on Image Processing, Lausanne, Switzerland, September 1996.
149. Member, International Scientific Committee, KoREMA'96, 41st Annual Conference, Opatija, Croatia, September 1996.
150. Member, Technical Program Committee, The Third International Conference on Electronics, Circuits, & Systems - ICECS'96, Rhodes, Greece, October 1996.
151. Member, Technical Program Committee, and Session Chairman, Session on "Image Coding I," 1997 IEEE International Conference on Acoustics, Speech & Signal Processing, Munich, Germany, April 1997.

152. Member, Advisory Committee, 1997 IEEE International Symposium on Circuits & Systems, Hong Kong, June 1997.
153. Session Chairman, Sessions on "Adaptive Filtering" and "Filters, Filter Banks, and Wavelets," 1997 IEEE International Symposium on Circuits & Systems, Hong Kong, June 1997.
154. Member, International Technical Program Committee, 1997 International Conference on Digital Signal Processing, Santorini, Greece, July 1997.
155. Member, Organizing Committee, 1997 IEEE Workshop on Nonlinear Digital Signal and Image Processing, Mackinac Island, Michigan, September 1997.
156. General Chairman, 1997 IEEE International Conference on Image Processing, Santa Barbara, California, October 1997.
157. Member, Technical Program Committee, International Conference on Computational Linguistics, Speech and Document Processing, Calcutta, India, February 1998.
158. Member, Technical Program Committee, IEEE Workshop on Content-Based Access of Image and Video Libraries, Santa Barbara, June 1998.
159. Member, Technical Program Committee, and Special Session Organizer & Chair, 1998 European Signal Processing Conference (EUSIPCO'98), Rhodes, Greece, September 1998.
160. Member, Technical Program Committee, 4th International Conference on Electronics, Circuits, and Systems (ICECS'98), Lisbon, Portugal, September 1998.
161. Member, Technical Program Committee, Conference on Electronic Imaging and Multimedia Systems, SPIE Symposium on Photonics China, September 1998.
162. Member, Technical Program Committee, and Session Organizer & Chair, Special Session on "Multimedia Services and Applications," IEEE 10th Tyrrhenian Workshop on Digital Communications, Ischia, Italy, September 15 -18, 1998.
163. Member, Technical Program Committee and Session Chair, Session on "Image Modeling", 1998 IEEE International Conference on Image Processing, Chicago, IL, October 1998.
164. Member, Technical Program Committee and Session Chair, Session on "Image Analysis", 1999 IEEE International Conference on Acoustics, Speech & Signal Processing, Phoenix, Arizona, March 1999.
165. Plenary Speaker, Plenary Session Commemorating the 50th Anniversary of the IEEE Circuits & Systems Society and Session Chair, Session on "Image Restoration", IEEE International Symposium on Circuits & Systems, Orlando, FL, May-June 1999.
166. Member, Technical Program Committee, IEEE-EURASIP Workshop on Nonlinear Signal and Image Processing, (NSIP'99), Antalya, Turkey, June 1999.
167. Member, Technical Program Committee, IEEE Workshop on Content-Based Access of Image and Video Libraries (CBAIVL-99), Fort Collins, CO, June 1999.
168. Plenary Speaker and Corresponding Member, 7th International Conference on Image Processing and Its Applications, Manchester, U.K., July 1999.
169. Member, International Program Committee, 10th International Conference on Image Analysis and Processing (ICIAP'99), Venice, Italy, September 1999.

170. Member, Scientific Committee, Telecommunications and Signal Processing Conference - TSP 99, Brno, Czech Republic, September 1999.
171. Member, Technical Program Committee, Sixth International IEEE Conference on Electronics, Circuits, and Systems (ICECS '99), Pafos, Cyprus, September 5-8, 1999.
172. USA Liaison and Member, Technical Program Committee, and Session Chair, Session on "Lossless Coding," 1999 IEEE International Conference on Image Processing, Kobe, Japan, October 1999.
173. Member, Technical Program Committee, 2000 IEEE International Conference on Acoustics, Speech & Signal Processing, Istanbul, Turkey, June 2000.
174. Member, Technical Program Committee, First International Workshop on Image and Signal Processing and Analysis (IWISPA 2000), Pula, Croatia, June 2000.
175. Member, Technical Program Committee, 2000 European Signal Processing Conference (EUSIPCO'2000), Tampere, Finland, September 2000.
176. Member, Technical Program Committee, 2000 IEEE International Conference on Image Processing, Vancouver, B.C., Canada, September 2000.
177. Member, Scientific Committee, 5th Conference on Neural Networks in Electrical Engineering, Belgrade, Yugoslavia, September 2000.
178. Member, International Advisory Committee, International Conference on Communications, Computers, and Devices (ICCCD-2000), Kharagpur, India, December 2000.
179. Member, Education Award Committee, IEEE Circuits & Systems Society, 2001.
180. Member, Technical Program Committee and Session Chairman, IEEE International Conference on Acoustics, Speech, & Signal Processing, Salt Lake City, Utah, May 2001.
181. Member, Technical Program Committee, Second International Symposium on Image and Signal Processing and Analysis (ISPA'01), Pula, Croatia, June 2001.
182. Member, Technical Program Committee, International Symposium on Signal Processing and Its Applications (ISSPA'2001), Kuala Lumpur, Malaysia, August 2001.
183. Member, Technical Program Committee, 8th IEEE International Conference on Electronics, Circuits, and Systems, Malta, September 2001.
184. Member, Organizing Committee, IEEE Workshop on Nonlinear Signal and Image Processing, (NSIP'01), Baltimore, Maryland, September 2001.
185. Member, Scientific Committee, 24th International Conference on Telecommunications and Signal Processing, Brno, Czech Republic, September 2001.
186. Member, Technical Program Committee, 2001 IEEE International Conference on Image Processing, Thessaloniki, Greece, October 2001.
187. Member, Organizing Committee, 4th International Conference on Digital Signal Processing and Its Applications (DSPA'2002), Moscow, Russia, February 2002.
188. Member, Technical Program Committee, 14th International Conference on Digital Signal Processing (DSP2002), Santorini, Greece, July 2002.
189. Member, Technical Program Committee, International Conference on Pattern Recognition, Quebec City, Quebec, Canada, August 2002.

190. Member, International Scientific Committee, 11th European Signal Processing Conference (EUSIPCO'2002), Toulouse, France, September 2002.
191. Member, Technical Program Committee, 2002 IEEE International Conference on Image Processing, Rochester, New York, September 2002.
192. Member, Technical Program Committee, 9th IEEE International Conference on Electronics, Circuits, and Systems, Dubrovnik, Croatia, September 2002.
193. Member, Technical Program Committee, SPIE Photonics China 2002 - Electronic Imaging and Multimedia Technology III, Shanghai, China, October 2002.
194. Chairman, Session on "Multimedia Networking and Multimedia Systems, 2002 IEEE Workshop on Multimedia Signal Processing, St. Thomas, U.S. Virgin Islands, December 2002.
195. Member, Technical Program Committee, 2003 IEEE International Conference on Acoustics, Speech, and Signal Processing, Hong Kong, May 2003.
196. Member, Technical Program Committee, 7th International Symposium on Signal Processing and Its Applications, Paris, France, June 2003.
197. Member, Technical Program Committee, IEEE International Workshop on Nonlinear Signal and Image Processing, Trieste, Italy, June 2003.
198. Member, Technical Program Committee, 10th International Conference on Computer Analysis of Images and Patterns (CAIP'2003), Groningen, The Netherlands, August 2003.
199. Member, Technical Program Committee, IEEE International Conference on Image Processing, Barcelona, Spain, September 2003.
200. Member, Technical Program Committee, 3rd International Symposium on Image and Signal Processing and Analysis, Rome, Italy, September 2003.
201. Member, Technical Program Committee, 10th IEEE International Conference on Electronics, Circuits, and Systems, Sharjah, United Arab Emirates, December 2003.
202. Co-General Chair, First International Symposium on Control, Communications, and Signal Processing, Hammamet, Tunisia, March 2004.
203. Member, International Technical Program Committee, 6th International Conference on Digital Signal Processing and Its Applications, Moscow, Russia, March 2005
204. Co-Chair, Tutorial Sessions, 2004 IEEE Midwest Symposium on Circuits & Systems, Hiroshima, Japan, July 2004.
205. Member, Scientific Committee, 27th International Conference on Telecommunications and Signal Processing, Brno, Czech Republic, September 2004.
206. Member, Technical Program Committee, IEEE International Conference on Image Processing, Singapore, October 2004.
207. Session Chair, IEEE International Conference on Image Processing, Singapore, October 2004.
208. Member, International Committee, Fifth IEEE International Caracas Conference on Devices, Circuits and Systems (ICDCS 2004), Dominican Republic, November 2004.
209. Member, Technical Program Committee, SPIE Conference on Electronic Imaging and Multimedia Technology IV, Beijing, China, November 2004.

210. Member, International Technical Program Committee, IEEE International Conference on Industrial Technology (ICIT04), Hammamet, Tunisia, December 2004.
211. Member, International Advisory Committee, International Conference on Signal Processing and Communications, Bangalore, India, December 2004.
212. Co-Chair, First International Workshop on Video Processing and Quality Metrics for Consumer Electronics, Scottsdale, AZ, January 2005.
213. Member, International Technical Program Committee, 7th International Conference on Digital Signal Processing and Its Applications, Moscow, Russia, March 2005.
214. Member, International Advisory Committee, 2005 International Conference on Communications, Circuits, and Systems, Hong Kong, May 2005.
215. Co-General Chair, International Workshop on Genomic Signal Processing, Bucharest, Romania, July 2005.
216. Co-General Chair, 7th International Symposium on Signals, Circuits, and Systems, Iasi, Romania, July 2005.
217. Member, Technical Program Committee, 8th International Symposium on Signal Processing and Its Applications, Sydney, Australia.
218. Member, Scientific Committee, 17th European Circuit Theory and Design Conference (ECCTD'2005), Cork, Ireland, September 2005.
219. Session Chair, 17th European Conference on Circuit Theory & Design (ECCTD'2005), Cork, Ireland, August-September 2005.
220. Co-Chair, Special Sessions Committee, IEEE International Conference on Image Processing, Genoa, Italy, September 2005.
221. Member, Technical Program Committee, 4th International Symposium on Image and Signal Processing and Analysis, Zagreb, Croatia, September 2005.
222. Member, Scientific Committee, IEEE Region 8 Conference EUROCON 2005, Belgrade, Serbia & Montenegro, November 2005.
223. Member, Technical Program Committee, "Visual Communications and Image Processing", SPIE/IS&T Electronic Imaging (EI) Conference, San Jose, CA, January 2006.
224. Member, International Technical Program Committee, 8th International Conference on Digital Signal Processing and Its Applications, Moscow, Russia, March 2006.
225. Member, International Advisory Board, International Conference on Computer and Communications Engineering, Gombak, Malaysia, May 2006.
226. Member, International Advisory Committee, International Conference on Communications, Circuits and Systems, Guilin, China, June 2006.
227. Co-Chair, Track on Image Processing and Multimedia, 49th International Midwest Symposium on Circuits & Systems, San Juan, Puerto Rico, September 2006.
228. Member, International Technical Program Committee, 8th Seminar on Neural Networks and Its applications in Electrical Engineering, Belgrade, Serbia, September 2006 (NEUREL-2006)
229. Member, International Advisory Board, International Conference on Computers and Devices for Communication, Kolkata, India, December 2006.

230. Co-General Chair, 8th International Symposium on Signals, Circuits, and Systems, Iasi, Romania, July 2007.
231. Member, International Advisory Board, 2007 IEEE International Symposium on Circuits & Systems, New Orleans, Louisiana.
232. Co-General Chair, 3rd International Symposium on Communications, Control, and Signal Processing, Malta, March 2008.
233. Member, International Advisory Board, 2009 IEEE International Symposium on Circuits & Systems, Beijing, China.

Professional Society Committee Activities

1. Member of the Editorial Board, IEEE Press, New York, New York (1972-76).
2. Member of the Editorial Board, Proceedings of the IEEE, New York (1977-1982).
3. Member, Task Force on Newsletter Editor's Manual, Publications Activities Board, IEEE (1968).
4. Member, Administrative Committee, IEEE Circuits and Systems Society (1972-75).
5. Chairman (1972-75) and Member (1977-79), Technical Committee on Education, IEEE Circuits and Systems Society.
6. Vice-Chairman (1970-71) and Chairman (1971-72), San Francisco Chapter, IEEE Circuits and Systems Society.
7. Member, Best Paper Awards Committee, IEEE Circuits and Systems Society (1970-75).
8. IEEE Press Books Coordinator, IEEE Circuits and Systems Society (1978-79, 1987-88).
9. Member, Steering Committee of the Asilomar Conference on Circuits, Systems, and Computers (1977 - 1985).
10. Member, Terman Award Committee, American Society for Engineering Education (1981-83).
11. Member, Technical Committee on Digital Signal Processing, IEEE Circuits and Systems Society (1981 - 1987, 1994 -).
12. Member, Technical Committee on Signal Processing, IEEE Group on Industrial Electronics and Control Instrumentation (1980-85).
13. President-Elect, IEEE Circuits and Systems Society (1985).
14. Member, IEEE Membership Development Committee (1985).
15. Chairman, Conference Procedures and Planning Committee, IEEE Circuits and Systems Society (1983-85).
16. Chairman, Publications Committee, IEEE Circuits and Systems Society (1984-85).
17. Chairman, Technical Activities Committee, IEEE Circuits and Systems Society (1984-85).
18. President, IEEE Circuits and Systems Society (1986).

19. Member, IEEE Technical Activities Board (1986).
20. Past-President, IEEE Circuits & Systems Society (1987).
21. Member, AT&T Foundation Award Committee, American Society for Engineering Education (1987).
22. Member, Technical Committee on Strategic Planning, IEEE Circuits & Systems Society (1988-89, 1989-90).
23. Vice-Chairman, IEEE International Workshop on Future Directions on Circuits, Systems and Signal Processing (1990).
24. Member, International Steering Committee, IEEE Asia-Pacific Conference on Circuits & Systems (1991-).
25. Member, Education Award Committee, IEEE Circuits and Systems Society (1990-93).
26. Member, Conference Board, IEEE Signal Processing Society (1995-98).
27. Member, Technical Committee on Image and Multidimensional Digital Signal Processing, IEEE Signal Processing Society (1995-2001).
28. Member, Board of Governors, IEEE Signal Processing Society (1997 - 99).
29. Member, Education Award Committee, IEEE Circuits & Systems Society (2001).
30. Member, Young Author Best Paper Award Committee, IEEE Circuits & Systems Society (2003).
31. Member, IEEE Transactions on image Processing Editor-in-Chief Selection Committee, 2006.

Other Professional Activities

1. Member, Technology Development Council, Santa Barbara Chamber of Commerce (1983-85).
2. Member, Advisory Council, George R. Brown School of Engineering, Rice University, Houston, Texas (1986-1989).
3. Member, Chancellor's Committee for the review of the activities and program of the Research School of Physical Sciences, Australian National University, Canberra, Australia (1987).
4. Member, National Science Foundation Site Visit Team for Engineering Research Center, Georgia Institute of Technology, Atlanta, GA, November 1987.
5. Member of NSERC (Natural Sciences and Engineering Research Council of Canada) Evaluation Committee for MICRONET - Network of Centres of Excellence on Microelectronic Devices, Circuits and Systems for Ultra Large Scale Integration, March 1989.
6. Member, Research Initiation Grants Review Panel, National Science Foundation, April 1990.
7. Member, Visiting Review Committee, Department of Electrical Engineering, Ecole Polytechnique Federale Lausanne (EPFL), Ecublens, Lausanne, Switzerland, July 1990.
8. Member, Visiting Review Committee, Department of Electrical Engineering, University of Southern California, Los Angeles, California, January 1991.

9. Member, Review Committee, Department of Electrical and Computer Engineering, University of Iowa, Iowa City, Iowa, December 1991.
10. Member, NSERC (Natural Sciences and Engineering Research Council of Canada) International Peer Review Committee of MICRONET - Network of Centre of Excellence on Microelectronic Devices, Circuits and Systems for Ultra Large Scale Integration, University of Toronto, Canada (1992 - 93).
11. Member, International Review Panel, Electronics and Communications Research Grants Awarded to Australian Universities, Australian Research Council, Australia, July 1993.
12. Member, National Science Foundation Site Visit Team for Infrastructure Grant, University of North Carolina, Chapel Hill, NC, April 1995.
13. Member, Advisory Council, Research Institute for Mathematics and Computing Science, University of Groningen, Groningen, The Netherlands (1995 -).
14. Member, Advisory Board, International Signal Processing Center, Tampere University of Technology, Tampere, Finland (1997 -).
15. External Assessor, Faculty of Engineering, Universiti Putra Malaysia, Serdang, Selangor, Malaysia (1997 - 2000).
16. Member, Review Panel, Division of Computer and Information Science Engineering, National Science Foundation, Washington, DC (March 2000).
17. Member of NSERC (Natural Sciences and Engineering Research Council of Canada) Evaluation Committee for IRIS (The Institute for Robotics and Intelligent Systems) and MICRONET (Network of Centres of Excellence on Microelectronic Devices, Circuits and Systems for Ultra Large Scale Integration), May 2001.
20. Member, Electrical Engineering Advisory Board, University of Southern California, Los Angeles, California (September 2003 – August 2005).
21. Member, Review Panel, Telehealth Technologies Development, National Institute of Biomedical Imaging and Bioengineering, Bethesda, MD (July 2003).
22. Vice-Chair, Doctoral Education Evaluation Team, Ministry of Education, Government of Finland, September 2005.
19. Member, Evaluation Committee for the Department of Electrical Engineering and Information Technologies, Technological University of Cyprus, Limassol, Cyprus, May 2006.

Keynote Lectures & Plenary Talks

1. *1968 Summer School on Circuit Theory*, Prague, Czechoslovakia (Sponsored by the Czechoslovak Academy of Science and International Radio Scientific Union), June 1968.
2. *1971 IEEE International Symposium on Electrical Network Theory*, London, England, September 1971.
3. *1971 Summer School on Circuit Theory*, Tale, Czechoslovakia (Sponsored by the Czechoslovak Academy of Science and International Radio Scientific Union), September 1971.
4. *8th IEEE Annual International Phoenix Conference on Computers & Communications*, Scottsdale, AZ, March 1989.
5. *6th International Symposium on Networks, Systems and Signal Processing*, Zagreb, Croatia, June 1989.

6. *URSI/IEEE 15th Conference on Radio Science*, Tampere, Finland, November 1989.
6. *International Symposium on Signal Processing and Its Applications - ISSPA'90*, Gold Coast, Australia, August 1990.
7. *38th IEEE Midwest Symposium on Circuits & Systems*, Rio de Janeiro, Brazil, August 1995.
8. *8th European Signal Processing Conference (EUSIPCO'96)*, Trieste, Italy, September 1996.
9. *Conference on Signal Processing, Communications, and Networking (SPCOM'97)*, Bangalore, India, July 1997.
10. *1999 IEEE International Symposium on Circuits & Systems*, Orlando, Florida, May 1999.
11. *7th IEE International Conference on Image Processing and Its Applications*, Manchester, England, July 1999.
12. *Scoula Nazionale di Dottorato in Elettronica*, Catania, Sicily, Italy, June 2001.
13. *National Meeting of Gruppo Nazionale di Elettronica*, Mondello, Sicily, Italy, June 2001.
14. *2nd International Symposium on Image and Signal Processing and Analysis (ISPA'01)*, Pula, Croatia, June 2001.
15. *15th European Circuit Theory and Design Conference*, Helsinki, Finland, August 2001.
16. *8th IEEE International Conference on Electronic Circuits and Systems*, Malta, September 2001.
17. *2nd International Conference JTEA'02*, Sousse North, Tunisia, March 2002.
18. *Silver Jubilee Symposium, IEEE Thailand Section*, Bangkok, Thailand, November 2002.
19. *5th International Conference and Exhibition on Digital Signal Processing and Its Applications*, Moscow, Russia, March 2003.
20. *International Conference on Devices, Circuits and Systems*, Vera Cruz, Mexico, June 2003.
21. *6th International Symposium on Circuits & Systems, Iasi, Romania, July 2003*.
22. *2004 International Conference on Communications, Circuits and Systems*, Chengdu, China, June 2004.
23. *47th IEEE International Midwest Symposium on Circuits & Systems*, Hiroshima, Japan, July 2004.
24. *2005 IEEE/EURASIP Workshop on Nonlinear Signal and Image Processing*, Sapporo, Japan, May 2005.
25. *IEEE EUROCON'2005*, Belgrade, Serbia and Montenegro, November 2005.

Awards and Honors

1. Elected Fellow of the Institute of Electrical and Electronics Engineers (IEEE) in 1973 “for contributions to active network theory and engineering education”.
2. 1973 F.E. Terman Award of the American Society for Engineering Education (ASEE) as an outstanding young engineering educator.

3. Listed in AMERICAN MEN AND WOMEN OF SCIENCE, WHO'S WHO IN AMERICA, WHO'S WHO IN THE WEST, DICTIONARY OF INTERNATIONAL BIOGRAPHY, MEN OF ACHIEVEMENT, WHO'S WHO IN ENGINEERING, WHO'S WHO IN SCIENCE & ENGINEERING.
4. Visiting Professor Award from the Japan Society for Promotion of Science (May 1973 - September 1973).
5. Visiting Professor Fellowship from the Deutsche Forschungsgemeinschaft (German Science Foundation) (September 1975 - December 1975).
6. Elected Fellow of the American Association for the Advancement of Science (AAAS) in January 1982 "for important contributions to analog and digital signal processing".
7. Organization of American States, Washington, D.C., International Travel Grant to visit and present invited lectures at the National Institute for Astrophysics, Optics and Electronics, Puebla, Pue, Mexico, 1983.
8. Advisor to the Department of Radio Engineering, Beijing University, Beijing, People's Republic of China, August 1983 (under a World Bank Development Program).
9. Visiting Fellow, Australian National University, Canberra, ACT, Australia (July 1982 - December 1982).
10. Irma L. Runyon Distinguished Lecturer, Texas A&M University, College Station, Texas, March 1984.
11. Served as an Opponent for Ph.D. Thesis Defense, Department of Electrical Engineering, Tampere University of Technology, Tampere, Finland, June 1984.
12. Fulbright Distinguished Professor Award for Brazil, August 1984.
13. Lansdowne Lecturer, University of Victoria, B.C., Canada, April 1985.
14. Honorary Professor, Northern Jiaotong University, Beijing, People's Republic of China, January 1985.
15. AT&T Foundation Award for Excellence in Instruction of Engineering Students, American Society for Engineering Education (ASEE), November 1985.
16. Distinguished Visiting Professor Award, Ain Shams University, Cairo, Egypt, March 1986.
17. Fulbright Distinguished Professor Award for Yugoslavia, August 1986.
18. Doctor Honoris Causa, Tampere University of Technology, Tampere, Finland, May 1987.
19. Fulbright Distinguished Professor Award for Turkey, September-October 1988.
20. Distinguished Visiting Professor, Kobe University, Kobe, Japan, November - December 1988.
21. 1988 Education Award of the IEEE Circuits & Systems Society for "contributions to and leadership in network theory and signal processing through doctoral theses and research, and tutorial articles, and books".
22. Alexander von Humboldt Distinguished Senior U.S. Scientist Award, 1989-90.
23. Distinguished Lecturer, IEEE Circuits & Systems Society, 1991-96.
24. Elected Fellow of the International Society for Optical Engineering (SPIE) in 1992 "in recognition of distinguished and valuable contributions to the field of optical engineering".

25. 1994 Harry Lynde Bradley Distinguished Lecturer, Virginia Polytechnic Institute and State University, Blacksburg, VA.
26. 1995 Technical Achievement Award of the IEEE Signal Processing Society for “fundamental and sustained contributions to signal processing research and graduate education”.
27. Visiting Professor Award from the Japan Society for Promotion of Science (March 1997).
28. Served as an Opponent for Ph.D. Thesis Defense, Norwegian University of Science and Technology, Trondheim, Norway, August 1997.
29. Honored as a pioneer in signal processing at the 1998 IEEE International Conference on Acoustics, Speech, and Signal Processing on the occasion of the 50-th anniversary of the IEEE Signal Processing Society.
30. Interviewed in 1998 by the IEEE for their oral history project and the taped interview containing my views on the most important developments in the areas I have worked is available in the IEEE's Oral History Collection.
31. 1999 Mac Van Valkenburg Society Award, IEEE Circuits & Systems Society for “fundamental contributions to the theory and teaching of active and digital filter theory, and dedicated and creative service to the IEEE Circuits & Systems Society”.
32. CAS Golden Jubilee Medal, IEEE Circuits & Systems Society, 1999.
33. IEEE Third Millennium Medal, 2000.
34. Appointed Academician, The Academy of Finland, March 2000.
35. Blumlein-Browne-Willans Premium, The Institution of Electrical Engineers, London, U.K., October 2000.
36. McGraw-Hill/Jacob Millman Award of the IEEE Education Society, October 2001 “for outstanding contributions to electrical engineering education through textbooks, research, and guiding and mentoring of students”.
37. 2001 Society Award of the IEEE Signal Processing Society “for important and significant contributions to analog and digital signal processing and image processing”.
38. 2001 IEEE Transactions on Circuits and Systems for Video Technology Best Paper Award.
39. Technical Achievement Award, European Association for Signal Processing (EURASIP), September 2002.
40. Elected Foreign Member, Croatian Academy of Sciences & Arts, Zagreb, Croatia, June 2002.
41. Elected Member, Norwegian Academy of Technological Sciences, Oslo, Norway, September 2002.
42. Elected Member, U.S. National Academy of Engineering, February 2003.
43. Elected Foreign Member, Academy of Engineering of Mexico, September 2003.
44. Doctor Honoris Causa, “Politehnica” University of Bucharest, Bucharest, Romania, October 2004.
45. SPIE Technical Achievement Award, The International Society for Optical Engineering, 2005.
46. University Medal, Slovak University of Technology, Bratislava, Slovakia, March 2005.
47. Honorary Professor, Technical University of Cluj-Napoca, Cluj-Napoca, Romania, July 2005.

48. Appointed Honorary Citizen of the City of Cluj-Napoca, Romania, July 2005.
49. 2005 Education Award, IEEE Signal Processing Society.
50. 2006 IEEE James H. Mulligan, Jr. Education Medal.
51. Elected Foreign Fellow, Indian National Academy of Engineering, September 2006.